SRINIVASA EDUCATIONAL SOCIETY'S
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
Approved by AICTE and Govt. of Andhra Pradesh, Accredited by NAAC(A Grade) | Recognized under 2(f) & 12(B) of UGC
Permanently Affiliated to JNTUK, Kakinada. A.P. | ISO 9001:2015, ISO 14001:2015 and ISO 50001:2018 Certified Institution
NH-16, Near Valluramma Temple, ONGOLE - 523 272, A.P., India, Ph.: 08592 278315, 9581456310 | www.pace.ac.in

Lr No: PACE (A)/ECS/2024-2025/IE/32 07-02-2025

CIRCULAR

All the III B.Tech (R21) II Semester Students are hereby informed that the First mid examinations is scheduled from 10-02-2025. The Time table of examinations is as given below.

The pattern of the descriptive examination: 3 questions to be answered by the student for 15 marks from the given 3 questions.

Timing of the Descriptive Examination : 10:30 am to 12:00 pm

Timing of the Online Examination : 12 noon onwards

Branch	Date & Day				
	10-02-2025 (Mon Day)	**11-02-2025 (Tue Day)**	**12-02-2025 (Wed Day)**	**13-02-2025 (Thur Day)**	**14-02-2025 (Fri Day)**
CIVIL	Design and Drawing of Steel Structures	Water Resources Engineering	Foundation Engineering	Introduction to Database Management Systems	Employability Skills
EEE	Power system Analysis	Switch gear and protection	Linear & Digital Ic Applications	Power Semiconductor Drives	Design Thinking for Innovation
ME	Metrology & Measure ments	Heat & Mass Transfer	Design of Machine Elements -II	Renewable Sources of Energy	Constitution of India
ECE	Microwave & Radar Engineering	Internet of Things	VLSI Design	Optical Communications / Digtal Image Processing	Intellectual Property Rights
CSE	Cryptography & Network Security	Advanced Java & Web Technologies	Automata Theory & Compiler Design	Introduction to Simulation Software	Entrepreneur ship and Innovation
CSIT	Cryptography & Network Security	Machine Learning	Automata Theory & Compiler Design	Management Science	Design Thinking for Innovation
IT	OOAD Using UML	Machine Learning	Design and Analysis of Algorithms	Management Science	Design Thinking for Innovation

Branch	Date & Day				
	10-02-2025 (Mon Day)	**11-02-2025 (Tue Day)**	**12-02-2025 (Wed Day)**	**13-02-2025 (Thur Day)**	**14-02-2025 (Fri Day)**
CSE (IOT& CSBT)	Sensor Network Technologies	Reinforcement Learning	Design and Analysis of Algorithms	Embedded Systems	Design Thinking for Innovation
AIDS	Data Science	Deep Learning	Design and Analysis of Algorithms	Introduction to Simulation Software	Design Thinking for Innovation
AIML	Data Science	Deep Learning	Design and Analysis of Algorithms	Introduction to Simulation Software	Design Thinking for Innovation
CSE (RL)	Cryptography & Network Security	Advanced Java & Web Technologies	Automata Theory & Compiler Design	IPR&PATENTS	Entrepreneurs hip and Innovation

NOTE:

i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately

ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual

CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272, ANDHRA PRADESH

PRINCIPAL

PRINCIPAL
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272, ANDHRA PRADESH

Copy to:

: ALL HOD's for circulation to the Staff & Students
: Coordinator, IQAC – for information
: Dean Academics - for information
: Administrative Officer- for information
: Transport I/C – for necessary action
: Office File
: Notice board at Exam Cell & System
: Web Design Coordinator