SRINIVASA EDUCATIONAL SOCIETY'S
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
Approved by AICTE and Govt. of Andhra Pradesh, Accredited by NAAC(A Grade) | Recognized under 2(f) & 12(B) of UGC
Permanently Affiliated to JNTUK, Kakinada. A.P. | ISO 9001:2015, ISO 14001:2015 and ISO 50001:2018 Certified Institution
NH-16, Near Valluramma Temple, ONGOLE - 523 272, A.P., India, Ph.: 08592 278315, 9581456310 | www.pace.ac.in

Lr No: PACE(A)/ECS/2025-26/EE/16 04-11-2025

CIRCULAR

All the students are hereby informed that III B.Tech I Semester Regular (2023 Admitted Batch) End Examinations will commence from **24-11-2025.** The Time Table for these examinations is given below.

Branch	**Timing of the Semester End Examination: 10:00 am to 1:00 pm**			
	Date & Day			
	24-11-2025 (Mon Day)	**26-11-2025 (Wed Day)**	**28-11-2025 (Fri Day)**	**01-12-2025 (Mon Day)**
CIVIL	Design and Drawing of Reinforced Concrete Structures	Engineering Hydrology	Geotechnical Engineering	Principles of Database Management Systems
EEE	Power Electronics	Digital Circuits	Power Systems-II	Signals and Systems
ME	Machine Tools & Metrology	Thermal Engineering	Design of Machine Elements	Renewable Energy Technologies
ECE	Analog and Digital IC Applications	Digital Communications	Antennas and Wave Propagation	Optical Communications
CSE	Data Warehousing and Data Mining	Computer Networks	Formal Languages and Automata Theory	Introduction to Simulation Software
CSIT	Data Warehousing and Data Mining	Computer Networks	Automata Theory and Compiler Design	Entrepreneurship Development and Venture Creation
IT	Data Warehousing and Data Mining	Computer Networks	Advanced Java	Entrepreneurship Development and Venture Creation
CSE (IOT& CSBT)	Designing the IOT	Wireless Sensor Networks	Cryptography & Network Security	Entrepreneurship Development and Venture Creation

AIDS	Data Warehousing and Data Mining	Principles of Machine Learning	Introduction to Data Science	Entrepreneurship Development and Venture Creation
AIML	Deep Learning	Computer Networks	Natural Language Processing	Entrepreneurship Development and Venture Creation
CSE (RL)	Data Warehousing and Data Mining	Computer Networks	Formal Languages and Automata Theory	Introduction to Simulation Software
EE (VLSIDT)	Basic VLSI Design	Digital System Design through HDL	Linear and Digital IC Applications	Introduction to Micro Processor and Micro controllers

NOTE:

i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately

ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual

CONTROLLER OF EXAMINATIONS

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PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272. ANDHRA PRADESH

PRINCIPAL

04/11/25

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