

DIGITAL SIGNAL PROCESSING LAB

LIST OF EXPERIMENTS:

To study the architecture of DSP chips – TMS 320C 5X/6X Instructions.

To verify linear convolution.

To verify the circular convolution.

To design FIR filter (LP/HP) using windowing technique

Using rectangular window

Using triangular window

Using Kaiser window

To Implement IIR filter (LP/HP) on DSP Processors

N-point FFT algorithm.

MATLAB program to generate sum of sinusoidal signals.

MATLAB program to find frequency response of analog LP/HP filters.

To compute power density spectrum of a sequence.

To find the FFT of given 1-D signal and plot.